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**INFORMATION DISCLOSURE STATEMENT
BY APPLICANT**

Applicant: PELLENS	
Appln. No.: 10/783,034	
Filing Date: February 23, 2004	
Examiner: Kevin V. Quinto	Group Art Unit: 2826

Date: August 15, 2007 Page 1 of 1

U.S. PATENT DOCUMENTS

Examiner's Initials*	Document Number	Date MM/YYYY	Name (Family Name of First Inventor)	Class	Sub Class	Filing Date (if appropriate)
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FOREIGN PATENT DOCUMENTS

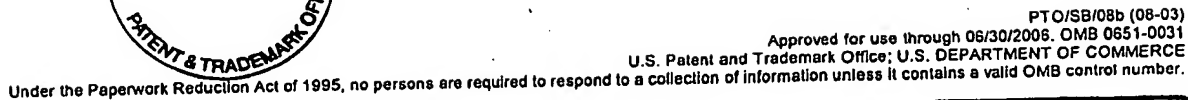
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OTHER (Including in this order Author, Title, Periodical Name, Date, Pertinent Pages, etc.)

/KQ/	YR	N. SAMOTO ET AL., Sub-0.1-um T-Shaped Gate Fabrication Technology Using Mixing-Layer Sidewalls in a Double-Layer Resist System, J. Vac. Sci Technology, American Vacuum Society, Vol. 12 (No. 6), pp. 3673-3676, (November/December 1994).			
	ZR				

Examiner /Kevin Quinto/ Date Considered: 10/28/2007

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

[illegible]

Examiner Signature	/Kevin Quinto/	Date Considered	10/28/2007
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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